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1. (Twice Amended) A high-frequency oscillation circuit comprising:
a closed loop circuit including at least one logic element, said at least one logic element having an input and an output, wherein said closed loop circuit begins at said output and returns to said output of said at least one logic element, said at least one logic element including a first logic element within said closed loop circuit;

another logic element external to said closed loop circuit;

a capacitor being disposed within said closed loop circuit;

a resistor being disposed within said closed loop circuit; and

a crystal oscillator for high frequency being disposed within said closed loop circuit, said crystal oscillator being connected in series with said capacitor and in parallel with said resistor and having a basic oscillation frequency of between 1 MHz to 500 MHz.

3. A high-frequency oscillation circuit as claimed in claim 1, further comprising another capacitor being disposed externally to and in serial connection with said closed loop circuit.

4. (Amended) A high-frequency oscillation circuit as claimed in claim 1, wherein said first logic element includes a high-speed TTL or CMOS.

5. (Twice Amended) A high-frequency oscillation circuit as claimed in claim 1, wherein said crystal oscillator of high frequency has a basic oscillation frequency of at least 30 MHz.

6. (Twice Amended) A high-frequency oscillation circuit comprising:
a closed loop circuit including at least one logic element, said at least one logic element having an input and an output, wherein said closed loop circuit begins at said output and returns to said output of said at least one logic element, wherein said at least one logic element includes a first logic element within said closed loop circuit and another logic element external to and in serial connection with said closed loop circuit, said first logic element including a high speed CMOS or a high speed TTL;

a capacitor being disposed within said closed loop circuit;

a resistor being disposed within said closed loop circuit; and

a crystal oscillator for high frequency being disposed within said closed loop circuit, said crystal oscillator being connected in series with said capacitor and in parallel with said resistor.

7. (Twice Amended) A high-frequency oscillation circuit as claimed in claim 1, wherein said first logic element includes a high-speed CMOS.

9. (Twice Amended) A high-frequency oscillation circuit comprising:
a closed loop circuit including at least one logic element, said at least one logic element having an input and an output, wherein said closed loop circuit begins at said output and returns to said output of said at least one logic element, said at least one logic element including a first logic element within said closed loop circuit;

another logic element external to said closed loop circuit;

a capacitor being disposed within said closed loop circuit;

a resistor being disposed within said closed loop circuit; and

a crystal oscillator for high frequency being disposed within said closed loop circuit, said crystal oscillator being connected in series with said capacitor and in parallel with said resistor, wherein said crystal oscillator is a sensor element for chemical measurement of a predetermined parameter.

11. (Amended) A high-frequency oscillation circuit comprising:
a closed loop circuit including at least one logic element, said at least one logic element having an input and an output, wherein said closed loop circuit begins at said output and returns to said output of said at least one logic element, wherein said logic element includes a high-speed TTL or CMOS;
a capacitor being disposed within said closed loop circuit;
a resistor being disposed within said closed loop circuit; and

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a crystal oscillator for high frequency being disposed within said closed loop circuit, said crystal oscillator being connected in series with said capacitor and in parallel with said resistor, said crystal oscillator having a basic oscillation frequency of 500 MHz or more.

12. (Amended) A high-frequency oscillation circuit as claimed in claim 1, wherein said another logic element being disposed externally to and in serial connection with said closed loop circuit.

13. A measuring instrument for measuring a predetermined parameter, said measuring instrument comprising:

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a closed loop, high frequency oscillation circuit including at least one logic element, said at least one logic element having an input and an output, wherein said closed loop circuit begins at said output and returns to said output of said at least one logic element;

a capacitor being disposed within said closed loop circuit;

a resistor being disposed within said closed loop circuit; and

a sensor for determining said predetermined parameter, wherein said sensor includes a crystal oscillator for high frequency being disposed within said closed loop circuit, said crystal oscillator being connected in series with said capacitor and in parallel with said resistor and having a natural oscillation

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frequency, a change in said natural oscillation frequency of said crystal oscillator being indicative of said predetermined parameter.

14. The measuring instrument for measuring said predetermined parameter according to claim 13, wherein said predetermined parameter includes at least one of a weight parameter, a viscosity parameter, and a film thickness parameter.

15. The measuring instrument for measuring said predetermined parameter according to claim 13, wherein said sensor is at least one of a weight sensor, a chemical sensor, a biosensor, a viscosity sensor, a film thickness meter, a gas sensor, a floating dust sensor, and an immunity sensor.

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16. The measuring instrument for measuring said parameter according to claim 15, said crystal oscillator having a basic oscillation frequency of 500 MHz or more.

17. The measuring instrument for measuring said parameter according to claim 15, said crystal oscillator having a basic oscillation frequency of between 1 MHz and 500 MHz.

18. The measuring instrument for measuring said parameter according to claim 13, said at least one logic element including a first logic element within said closed loop circuit and another logic element external to said closed loop circuit.

19. The measuring instrument for measuring said parameter according to claim 13, wherein said first logic element includes a high-speed CMOS.

20. The measuring instrument for measuring said parameter according to claim 13, wherein said logic element includes a high-speed TTL or CMOS.
